MINIATURIZATION FOR SPACE EXPLORATION

APPLIED NANOTECHNOLOGY AND MICROSYSTEMS

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NASA (JPL R&T&D), DARPA, and the SKULL BASE INSTITUTE
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INTRODUCTION

• Nano and Micro Systems group charter
  - To develop application-driven micro and nano devices/techniques for *in situ* as well as remote planetary exploration

• Application examples
  - Planetary mineralogy, Spacecraft G&N/precision pointing of instruments, Extreme environment sensors and electronics, Microimaging, Heterodyne Spectroscopy
  - Ideally related to NASA science missions (earth, planetary, astro/physics)
  - Many developments are multi-use (Space, Defense, Commercial)
OUTLINE

• Vacuum Microelectronics/Instruments Using CNTs
  - Field emission of electrons
  - High current density CNT bundle arrays
  - Applications
    ▪ X-rays for Mineralogy
    ▪ 700°C Logic Gates

• Distributed Capacitor (DisC) Sensor for Sample Verification
  - Need and rationale
  - Design and Test Results
FIELD EMISSION OF ELECTRONS

- Electron sources are fundamental components of many analytical instruments and certainly of vacuum electronic devices.

- Miniaturization of analytical instruments, for example, enables larger science payload.

- Vacuum electronics in combination with Silicon Micromachining enables miniature extreme environment components.

- Traditionally popular thermionic electron sources (boil off electrons at >1200°C) are power hungry, bulky, need thermal management, and are not conducive for miniaturization.

- **Cold cathodes is the answer…but which one?**
FIELD EMISSION OF ELECTRONS

Principle

Emission of electrons into vacuum because of tunneling when the potential barrier is distorted using an externally applied electric field.

\[ E = \frac{V}{d} \]

\[ E = \gamma \left( \frac{V}{d} \right) \]
FIELD EMISSION OF ELECTRONS

Fowler-Nordheim Equation

\[ I = 1.54 \times 10^{-6} \frac{\gamma^2 A_e}{\phi} \cdot \left( \frac{V}{d} \right)^2 \cdot e \left( -6.8 \times 10^7 \frac{\phi^{3/2} \cdot d}{\gamma V} \right) \]

\( \ln \left( \frac{I}{V^2} \right) = \ln(a) - \frac{b}{V} \)

“I \propto \gamma^2

“\( \gamma \)” is the key!

I : Emission current (A)
V: Biasing voltage (V) ; d : gap
a, b: constants
\( \phi \): Work function
\( \gamma \): Field enhancement factor
\( A_e \): Emission area
FIELD EMISSION OF ELECTRONS

- Sharp metallic tips or metal-coated Silicon tips are commonly used cold cathodes.

- They are sensitive to poor vacuums- very short lifetimes, and tips sputter easily.

- Most miniaturization applications using cold cathodes need vacuum packaging of microcavities.

  - Typical vacuums achieved is $10^{-6}$ to $10^{-5}$ Torr.

- Carbon nanotubes seem robust to such poor vacuums- redundancy, conducive emission mechanism, ease of synthesis, low emission threshold, and higher current density per tip compared to other types of cold cathodes.
FIELD EMISSION OF ELECTRONS

Simulation of field penetration
by L. Nilsson et al
APL 76(15) 2071-2073 (2000)

\[ I \propto \gamma^2 \]

\[ \gamma \propto \text{Aspect Ratio (reciprocal of the tip radius)} \]

\[ \gamma_s \propto \text{Reciprocal of CNT number density} \]

\[ d = 2h \quad \text{(simulation)} \]

\[ d = h \quad \text{(experiment)} \]


J.S. Suh et al, APL, 80, 2392-2394 (2002)

Achieving high-current density is really an OPTIMIZATION problem.
FIELD EMISSION OF ELECTRONS

- High electron emission from a single CNT (30 to 100 nA) does not scale up with increased number of CNTs on a sample.

- Electrostatic screening and number density limit emission levels.

- But, CNTs are great for application in miniature systems that have poor vacuums (10^{-4} to 10^{-5} Torr)

- Optimum arrangement needed.
CNT bundles of 1-2 μm diameter spaced 5 μm edge-to-edge produced the highest field emission current.

This arrangement is being used in multiple applications routinely producing 2 to 10 A/cm² current density at low fields of 4 to 8 V/μm.
HIGH-CURRENT DENSITY CNT BUNDLE ARRAYS

Repeatable High Current Density

\[ \gamma > 7000 \quad \gamma < 4000 \]

Range of current density = 10 to 15 A/cm²

2x10⁻⁵ Torr; 1 mA = 12.7 A/cm²; 785 μA = 10 A/cm²

Monolithic Gate Integration
APPLICATIONS
APPLICATIONS: X-rays for Mineralogy

The Importance

Courtesy: A. Yen (JPL)
APPLICATIONS: X-rays for Mineralogy

Planetary Instrument

Design conceived/developed by Dr. D. Blake et al of NASA-Ames

Schematic is somewhat similar to the CheMin instrument on Mars Science Laboratory.
APPLIEDTIONS: X-rays for Mineralogy

X-Ray Based Techniques

- Accelerated electrons bombard with a metal target to produce a continuous X-ray spectrum (Bremsstrahlung) as well as characteristic X-ray lines.

- The upper limit of the Bremsstrahlung radiation energy is limited by the acceleration voltage (Duane-Hunt Law).

![Diagram of X-ray generation process]
APPLICATIONS: X-rays for Mineralogy

Why CNTs?

- **No heater**: Save on power, mass, volume
- **Small source**: Easier focusing >> Smaller spot size >> Sharper diffraction patterns
- **Lower Voltage of Operation**: 15 kV in stead of 30 to 50 kV! {lower arcing probability}
- **Higher current density helps**: Faster data collection (only detector speed limited)
- **Especially important on Venus** where shorter integration times would be required (mission life would be in hours).
APPLICATIONS: X-rays for Mineralogy

CNT X-ray Tube

- Cu-anode (A)
- Gate 1 (G1)
- CNT-cathode (K)

-15.4 kV
-16.4 kV

~ 5 cm

φ 1.5 cm

APPLICATIONS: X-rays for Mineralogy
APPLICATIONS: X-rays for Mineralogy

CNT X-ray Tube

- Uses shaped Cobalt or Copper anode
- CNT bundle arrays are mounted on a screw-on platform
- Electron optics not optimized
- Operates between 15 kV to 20 kV acceleration voltage
- Emitted current at the cathode is in the range of 15 μA to 50 μA
- Max. photon flux produced so far ~ $8.8 \times 10^4 /s$
  (1-mm φ aperture)

Emission Efficiency

$$\eta = \frac{I_A}{I_K}$$
APPLICATIONS: X-rays for Mineralogy

CNT X-ray Tube

\[ K_\alpha = 8.8 \times 10^4 \text{ /s} \]
\[ K_\beta = 1.9 \times 10^4 \text{ /s} \]
\[ L_\alpha = 1.6 \times 10^4 \text{ /s} \]

\[ I_A \sim 3 \mu A (\eta \sim 20\%); V_A \sim 20.0 \text{ kV}; V_G = 2 \text{ kV}; \text{Measured photon fluxes through a 200-\textmu m } \phi \text{ collimator are shown in the inset.} \]

Co K\text{\textalpha} (6.92 \text{ keV}) and K\text{\textbeta} (7.69 \text{ keV})

\[ K_\alpha: 6.5 \times 10^3 \text{ /s-mm}^2 \]
\[ K_\beta: 716.8 \text{ /s-mm}^2 \]
\[ L_\alpha: 489 \text{ /s-mm}^2 \]

\[ I_A \sim 15 \mu A (\eta \sim 30\%); V_A \sim 16.4 \text{ kV}; \text{Measured photon fluxes through a 1-mm } \phi \text{ collimator are shown in the inset.} \]

Cu K\text{\textalpha} (8.04 \text{ keV}) and K\text{\textbeta} (8.91 \text{ keV}
APPLICATIONS: 700° C Logic Gates

• To fulfill the need for extreme environment electronics (700° C and radiation insensitive or hard)

• State-of-the-art: Solid-State devices; demonstrated up to 500° C and tens of Mega Rads (limited component demonstrations).

• NEMS computational components demonstrated up to 600° C (Case Western).

• JPL technology: “Digital” Vacuum Microelectronics – *programmable logic gate demonstrated at 700° C (DC switching)*
  - Turning the “clock” back a “bit” to the tube days; merging micromachining, nanotube field emission and vacuum packaging techniques.

• **FIRST OF ITS KIND** device demonstrated under a DARPA Seedling.
APPLICATIONS: 700° C Logic Gates

Inverse Majority Gate Device Schematic

Operational Schematic

Truth Table

<table>
<thead>
<tr>
<th>Number of Logic States</th>
<th>Gate 1</th>
<th>Gate 2</th>
<th>Gate 3</th>
<th>Output</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>1</td>
</tr>
<tr>
<td>2</td>
<td>0</td>
<td>0</td>
<td>1</td>
<td>1</td>
</tr>
<tr>
<td>3</td>
<td>0</td>
<td>1</td>
<td>0</td>
<td>1</td>
</tr>
<tr>
<td>4</td>
<td>0</td>
<td>1</td>
<td>1</td>
<td>0</td>
</tr>
<tr>
<td>5</td>
<td>1</td>
<td>0</td>
<td>0</td>
<td>1</td>
</tr>
<tr>
<td>6</td>
<td>1</td>
<td>0</td>
<td>1</td>
<td>0</td>
</tr>
<tr>
<td>7</td>
<td>1</td>
<td>1</td>
<td>0</td>
<td>0</td>
</tr>
<tr>
<td>8</td>
<td>1</td>
<td>1</td>
<td>1</td>
<td>0</td>
</tr>
</tbody>
</table>
APPLICATIONS: 700° C Logic Gates

Three-Gate Operation

<table>
<thead>
<tr>
<th>Va (V)</th>
<th>Vg1 (V)</th>
<th>Vg2 (V)</th>
<th>Vg3 (V)</th>
<th>Ia (nA)</th>
<th>O/P State</th>
</tr>
</thead>
<tbody>
<tr>
<td>50</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>3.2</td>
<td>1</td>
</tr>
<tr>
<td>50</td>
<td>10</td>
<td>0</td>
<td>0</td>
<td>2.8</td>
<td>1</td>
</tr>
<tr>
<td>50</td>
<td>10</td>
<td>10</td>
<td>0</td>
<td>0.7</td>
<td>0</td>
</tr>
<tr>
<td>50</td>
<td>10</td>
<td>10</td>
<td>10</td>
<td>0</td>
<td>0</td>
</tr>
<tr>
<td>50</td>
<td>0</td>
<td>10</td>
<td>0</td>
<td>3.6</td>
<td>1</td>
</tr>
<tr>
<td>50</td>
<td>0</td>
<td>10</td>
<td>10</td>
<td>0.27</td>
<td>0</td>
</tr>
<tr>
<td>50</td>
<td>0</td>
<td>0</td>
<td>10</td>
<td>2.5</td>
<td>1</td>
</tr>
<tr>
<td>50</td>
<td>10</td>
<td>0</td>
<td>10</td>
<td>0.35</td>
<td>0</td>
</tr>
</tbody>
</table>
### APPLICATIONS: 700° C Logic Gates

<table>
<thead>
<tr>
<th>For a Full Adder</th>
<th>Solid State (0.18 μm process)</th>
<th>Solid State (0.09 μm process)</th>
<th>IMG-based (0.5 μm process)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Footprint (μm)</td>
<td>13.86 x 5.4</td>
<td>8.12 x 2.52</td>
<td>18 x 4</td>
</tr>
</tbody>
</table>

- Traditional vacuum tubes operate at 1300° to 1500° C, so are natural choices for high-temperature electronics.
- Device can be designed for smaller footprint.
- Switching frequency- 10-100 GHz possible; *limited only by the K-A electron transit time*.
- Low level current (tens of nA) operation ensures long operational lifetime (10,000 hours tested for CNT flat panel displays).
- CNT emission more stable at 700° C (<1% variation tested).

**Miller Capacitance**

![Miller Capacitance Graph](image)
SUMMARY

- Carbon nanotube bundle arrays have been made to realize field emission sources for high current density applications.

- These sources act as the fundamental components of miniature analytical devices and vacuum microelectronics.

- Some applications such as a miniature X-ray tube, and “digital” vacuum electronics for high temperature applications have been developed.
DISTRIBUTED CAPACITOR (DISC) SENSOR FOR SAMPLE VERIFICATION

Sample verification sensors would be needed to ensure success of robotic sample return missions.
NEED: Past Sample Return Missions

Human Missions: Sample collected and verified by astronauts

- Apollo mission sample collection case (1969-1972)

Unmanned Robotic Missions: Sample quantity verified only after return to Earth

- Russian Luna-24 sample collector (Launched 1976)
- Stardust Aerogel sample collector (Launched 1999)
- JAXA's Hayabusa re-entry vehicle (Launched 2003)
- Genesis solar wind particles collector (Launched 2001)
NEED: SVS would maximize science return

Previous Unmanned Planetary Sample Return Missions

Launch from Earth
Outbound Flight
Approach/EDL to planet/moon
Sample Collection
Depart from planet/moon
Inbound flight
EDL to Earth
Sample Analysis (Sample Verification)

Sample Return Missions with Sample Verification

Launch from Earth
Outbound Flight
Approach/EDL to planet/moon
Sample Collection
Sample verification
Depart from planet/moon
Inbound flight
EDL to Earth
Sample Analysis

Re-attempt possible for maximized science return!
Possible Sample Verification Methods

**Spring-Mass Method**
- Relatively simple
- Requires gravity (not applicable to Asteroid)

**Optical Imaging Method**
- Color and shape observed
- Lens may be obscured by dust
- Mass not measurable

**Inertia Method**
(Newton’s 2\textsuperscript{nd} law of motion)
\[ F = m \frac{dv}{dt} \]

- Applicable to Asteroid (microgravity)
- Requires dynamic thrust

*Used on ISS for astronaut weight measurement*  
*Space Linear Acceleration Mass Measurement Device (SLAMMD)*
Sample Verification System (SVS) Requirements

- **Contamination issues**
  - Even small contamination could impact isotopic age determination.
  - All surfaces that may touch sample must be made with proven materials. Only few materials such as SUS304 and AL6061 would be allowed.

- **Harsh environment**
  - Survival temperature, -95C to +60C on moon.

- **Measurement accuracy / range**
  - Measure 50g +/-10g in lunar gravity.
  - Range 1000~3000g.

- **Robustness**
  - Launch and EDL shock tolerance
    - Vibration 10~60G.
Distributed Capacitance (DisC) Sensor Concept

- Elastic membrane and rigid substrate forms variable (semi-) parallel plate capacitor.

- Capacitance is inversely proportional to gap distance.

\[ C = \varepsilon \frac{A}{d} \]

- As sample accumulates, elastic membrane deform and capacitance increases.

- Robustness: When excessive weight/shock is applied, elastic membrane is stopped/protected by rigid substrate
**SVS DisC Sensor: First Prototype**

- **Plate (SUS304)**
- **Spacer (Silicon)**
- **Pattern of concentric capacitors**
- **Substrate (glass)**
- **Reference capacitor**

**Issues with first prototype:**
- Observed thermal drift due to CTE mismatch.
- Front-to-back wiring deemed weak.

**Fabrication method**
1. Silicon/glass anodic bonding
2. DRIE Silicon
3. Evaporate electrode pattern
Simulation vs. Experiment

- Once center channel touch, the rate of increase slows at outer channels.
- This is because top plate and electrode contact is not simulated.
- Otherwise, the behavior looks similar.

FEM simulation

Experimental result (Uniform loading with BB pellets)
SVS DisC Sensor: Second Prototype

Improvements over first prototype

- Use Polyimide PWB as substrate:
  - Distributed capacitor electrodes on top side
  - Circuit components on bottom side
- CTE mismatch absorbed by allowing plates to slip
- Use more flight-approved components

(Cross-sectional view is not to scale)
Electronics: How it works

- Current source charges selected SVS capacitive electrode
- Voltage on capacitor compared against reference with comparator
- Microcontroller counts charge time for capacitive measurement
Second Prototype Assembly

Backside of PWB

Front side of PWB

SUS304 Top plate is placed

Assembled unit
Non-Uniform Loading

100g weight (20mm diameter footprint) placed at center

100g weight (20mm diameter footprint) placed 20mm off-center

(z-axis scaling is exaggerated)

- Localized load may cause measurement error.
- Statistical approach coupled with robotic arm agitation to minimize measurement error (shown next page).
Sensor Calibration with Rock

- Repeated test with canister agitation provides improved measurement accuracy.
Proof-of-principle sample verification system (DisC sensor) developed for Lunar sample return missions. Sensor designed for harsh environment and robustness. Sensitivity depends on plate thickness and initial gap width: typ. 0.1~1pF/gram. Calibration with rock sample showed good statistical accuracy. Environmental tests (Thermal vacuum, shock, etc.) planned.
Thank You!