

## ON-CHIP p-MOSFET DOSIMETRY

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### ABSTRACT

The dosimeter consists of a p-MOSFET that is operated in a constant current mode, allowing the temperature effects on channel mobility and threshold voltage to cancel each other. The objective of this design is to minimize temperature effects and emphasize the dose dependence. The p-MOSFET's have a closed geometry gate eliminating the source to drain bird's beak leakage path. The dose dependence of transconductance is shown to be small, and can be ignored.

## SUMMARY

This paper describes the "m-chip" p-MOSFET radiation dosimeter proposed for use on all flight CMOS ASIC chips. Dosimetry measured via p-MOSFET threshold voltage shifts is influenced by two second order effects: (a) the temperature sensitivity of the transconductance factor,  $KP$ , and the threshold voltage,  $V_T$ , and (b) the source to drain bird's beak leakage current [1,2,3]. The goal in developing the advanced P-MOS101'1" dosimeter was to minimize or eliminate these effects.

The p-MOSFETs were measured in chip form using an hp4062 parametric test system with a hotchuck. Data for chip 1, dosimeter PR1, is shown in Table 1. The p-MOSFET 10 be flown on the Space Technology Research Vehicle (STRV) is operated in the saturation region which is insured by connecting the gate to the drain as shown in figure 1. In the saturation region, drain current is given by:

$$(1) I_p = (\beta/2) (V - V_T)^2 / [1 - O(V - V_T)]$$

where,  $\beta = KP \cdot W/J$ ,  $KP = \mu \cdot C_o$ , and  $V_T$  is the absolute value of the p-MOSFET threshold voltage. The other parameters are  $W$  and  $J$ , the as drawn channel width and length respectively,  $\mu$  is the channel mobility,  $C_o$  is the gate oxide capacitance per unit area,  $O$  is the mobility electric field dependence, and  $V$  is the gate, or output, voltage. Applying the Taylor series expansion to the  $O$  term and letting  $O \cdot V_T$  terms vanish, a linear equation is derived for the square root of the drain current.

$$(2) \sqrt{I_p} = \sqrt{(\beta/2) \cdot V_T + v - (o/?)(V^2)}$$

This equation, and the data from "Table 1, is plotted in figure 2, identifying the temperature independent drain current. The temperature dependence of the IV curves shown in figure 2 indicate that temperature effects on p-MOSFET dosimeters can be minimized by operating the p-MOSFET at a certain fixed current value., termed  $I_o$ . The above square-law relationship, Equation 1, with  $O = 0$ , is rewritten in terms of the output voltage:

$$(3) V = V_T + (2I_p/\beta)^{1/2}$$

The output voltage temperature sensitivity is found by differentiating Equation 3 with respect to temperature and evaluating at  $I_o$ :

$$(4) V_{T0} = V_T - (I_o/2)^{1/2} \cdot \beta_{T0} / \beta_0^{3/2}$$

where  $V_{T0} = \partial V / \partial T_{T0}$ ,  $V_T = \partial V_T / \partial T_{T0} = -1.752 \text{ mV}/^\circ\text{C}$ ,  $\beta_{T0} = \partial \beta / \partial T_{T0} = -n \cdot \beta_0 / T_0$  and  $n = 1.549$ .  $\beta_0$  is the value of  $\beta$  at  $T = T_0$ . Solving Equation 4 for  $I_o$  at  $V_{T0} = 0$  defines the temperature independent operating point:

$$(5) I_o = 2\beta_0^3 (V_T / \beta_{T0})^2$$

For the data given in Table 1, for  $T_0 = 303 \text{ K}$  or  $30^\circ\text{C}$ ,  $I_o = 189.9 \mu\text{A}$  and  $\sqrt{I_o} = 13.78 \sqrt{\mu\text{A}}$  as shown in figure 2. The temperature independent drain current,  $I_o$ , depends on the as drawn geometry because  $\beta = KP \cdot W/J$ . The design space for these pMOSFET dosimeters is shown in figure 3. The closed geometry gate is shown in Figure 4, The calibration curve, or damage factor,  $V_T$  vs  $I_p$  curve, for the p-MOSFET dosimeter on the proton detector chip is shown in figure 5 [4]. The damage factor is given for the line connecting 0 krad and the 8 day anneal point at 100 had.

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Table 1. p-MOSFET dosimeter data as a function of temperature using hp4062 with hotchuck for chip 4, transistor PR1, with  $W/L = 188.5/6 \mu\text{m}/\mu\text{m}$ .

T (°C)	$V_T$ (V)	$\beta$ ( $\mu\text{A}/\text{V}^2$ )	$\theta$ (1/V)
30	0.875	809	0.098
55	0.828	694	0.097
80	0.785	629	0.082

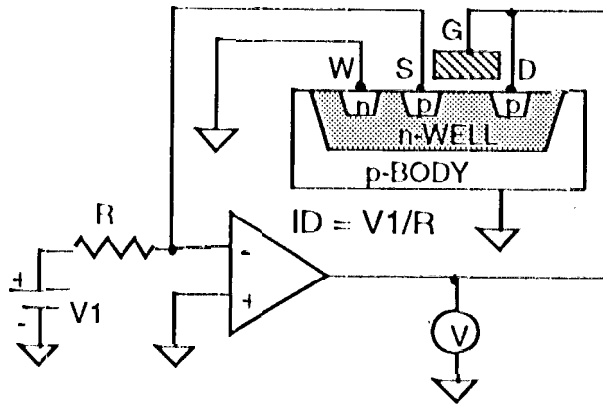


Figure 1. p-MOSFET dosimeter with a constant current source,  $I_D = I_0$ , set to minimize temperature variation. The gate voltage is proportional to the total dose,

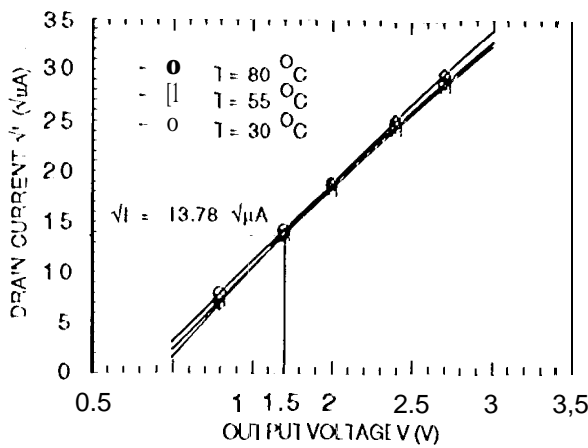


Figure 2. Current-voltage dependence of the p-MOSFET dosimeter showing the temperature independent point at  $\sqrt{I_0} = 13.78 \sqrt{\mu\text{A}}$ .

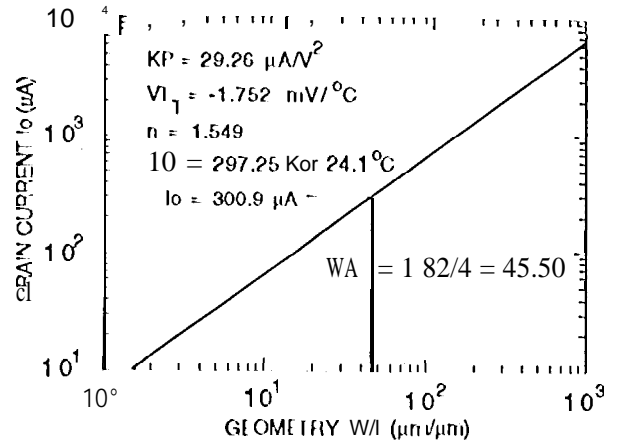


Figure 3. Design space for pMOSFET dosimeters showing the temperature independent drain current as a function of geometry,

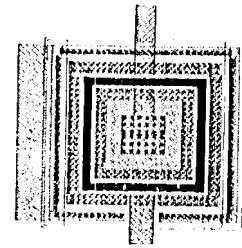


Figure 4. STRV SRAM proton detector ASIC p-MOSFET, MP4, layout, where  $W/L = 182/4 \mu\text{m}/\mu\text{m}$ , showing the closed-geometry gate.

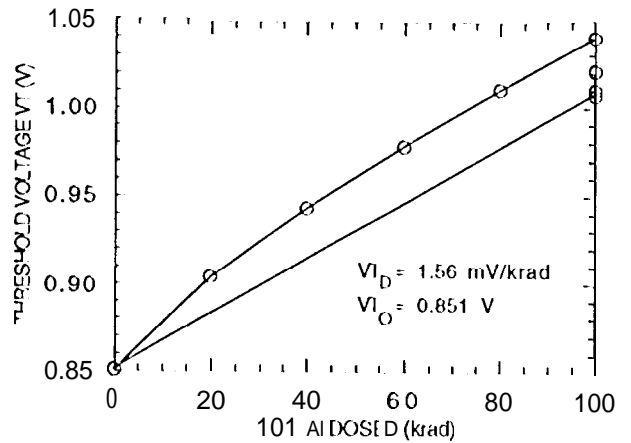


Figure 5. STRV ASIC p-MOSFET, MP4, calibration curve. Annealing after 0, 2, 5 and 8 days at 100 krad is shown.