

## User Support for an MPP, JPL Begins the Process

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At Supercomputing 92, it was announced that JPL/Caltech had entered into a collaborative agreement with CRAY Research to become one of the four "Cray Centers of Excellence in Parallel Computing". Under the agreement a T3D will be installed at JPL in the fall of 1993. This computer will have 256 processors, 512 MW of memory and will be capable of peak speeds of 38 gigaflops. Preliminary user services planning for the new system is beginning. This presentation will go through some general questions expected to help us determine how we can best serve users on the new system.