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JPL CMOS Active Pixel Sensor Technology

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A bstract

This paper will present the JPL-developed complementary metal-oxide-semiconductor (CMOS) active pixel sensor (APS) technology. The CMOS APS has achieved performance comparable to charge coupled devices, yet features ultra low power operation, random access readout, on-chip timing and control, and on-chip analog to digital converter. This paper will review previously published open literature.