This memo provides the abstract for an NEPP Conference presentation “Flight Qualifying SoC Devices” I plan to present on May 15, 2001.

abstract:

SoC devices require different approaches to the qualification process in order to address: the device complexity, integration of design elements of different forms, embedded memory elements, embedded software, the inclusion of analog elements and the difficulty in attaining test access. This paper presents an approach to meeting these challenges for spacecraft avionics applications.

cc: Choon Lee